

REMARKS

Claims 1-16, and 18 are presently pending. Claims 17, and 19-26 are cancelled without prejudice. Claims 1-16 and 18 were rejected.

Claims 6, 7, 9, and 15-18 were rejected under 35 U.S.C. 112, second paragraph. Assignee has amended claims 2 and 11, and cancelled claim 17, and it is believed that based on these amendments, these rejections are overcome.

Claims 1 and 10 were rejected under 35 U.S.C. 102(e) as being anticipated by Alvarez (U.S. Patent Publication 2006/0193383).

Claim 1 is amended to include, among other limitations, "a display engine for receiving the decoded parameters from the parameter buffers and providing the decoded images for display using the decoded parameters stored in the parameter buffers". Claims 10 is amended to include, among other limitations, "a display engine connected to the image buffers and the parameter buffers and configured to receive the decoded parameters from the parameter buffers and providing the decoded images for display using the decoded parameters stored in the parameter buffers".

Examiner indicated that Alvarez discloses "parameters buffers (i.e., 316 of Figure 2, and see paragraphs [0040], [0043], [0061], [0062]) ... display engine (i.e., 118, 120 of Figure 1 and 318 of Figure 3, see paragraphs [0026], [0039], [0040]) connected to the image buffers and the parameter buffers and configured to receive the decoder parameters from the parameter buffers and display the decoded images based on the decoded parameters...". Office Action at 3.

Assignee respectfully traverses the rejection. "The digital video decoding system 300 of [Alvarez] Fig. 3 can illustratively be employed to implement the digital video decoder 116 of Figs. 1 and 2." Alvarez, [0032]. Alvarez, Figure 3, discloses a decoder memory 316. Assignee particularly calls Examiner's attention to Alvarez, [0040], "Decoder memory 316 is used to store macroblock data and other time-critical data used during the decode process." (Emphasis Added).

In contrast, claims 1 and 10 are directed to "a display engine for receiving the decoded parameters from the parameter buffers and providing the decoded images for display using the decoded parameters stored in the parameter buffers" and "a display engine connected to the image buffers and the parameter buffers and configured to receive the decoded parameters from the parameter buffers and providing the decoded images for display using the decoded parameters stored in the parameter buffers", respectively. It is noted that if Examiner reads the "display engine" on (118, 120 of Figure 1 and 318 of Figure 3) there is no teaching that 118, 120 of Figure 1 and 318 of Figure 3 receive "decoded parameters" from decoder memory 316, especially when, "Decoder memory 316 is used to store macroblock data and other time-critical data used during the decode process." (Emphasis Added).

Accordingly, Examiner is requested to withdraw the rejection to claims 1 and 10, 2-9, 11-16, and 18. Additionally, claim 1 is further amended to include "parameter buffers for storing the decoded parameters associated with the images, wherein the decoded parameters include at least one parameters selected from a group consisting of presentation time stamp, top field first, and

repeat first field". It is respectfully submitted that Alvarez does not teach the foregoing. As noted above, Examiner has read the "parameter buffer" limitation on "decoder memory 316" in Alvarez. However, Assignee respectfully submits that Alvarez does not teach "storing the decoded parameters associated with the images, wherein the decoded parameters include at least one parameters selected from a group consisting of presentation time stamp, top field first, and repeat first field", especially when "Decoder memory 316 is used to store macroblock data and other time-critical data used during the decode process."

Conclusion

For at least the foregoing reasons, Assignee respectfully submits that each of the pending claims are in a condition for allowance, and Examiner is kindly requested to pass this case to issuance.

RESPECTFULLY SUBMITTED,



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